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| APPLICATION NO.                                                                                                      | FILING DATE | FIRST NAMED INVENTOR | ATTORNEY DOCKET NO. | CONFIRMATION NO. |
|----------------------------------------------------------------------------------------------------------------------|-------------|----------------------|---------------------|------------------|
| 10/604,173                                                                                                           | 06/30/2003  | SHI-HSIANG LU        | 10873-US-PA         | 1172             |
| 31561                                                                                                                | 7590        | 03/07/2006           | EXAMINER            |                  |
| JIANQ CHYUN INTELLECTUAL PROPERTY OFFICE<br>7 FLOOR-1, NO. 100<br>ROOSEVELT ROAD, SECTION 2<br>TAIPEI, 100<br>TAIWAN |             |                      | WU, XIAO MIN        |                  |
|                                                                                                                      |             |                      | ART UNIT            | PAPER NUMBER     |
|                                                                                                                      |             |                      | 2674                |                  |

DATE MAILED: 03/07/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

|                              |                                      |                                  |  |
|------------------------------|--------------------------------------|----------------------------------|--|
| <b>Office Action Summary</b> | <b>Application No.</b><br>10/604,173 | <b>Applicant(s)</b><br>LU ET AL. |  |
|                              | <b>Examiner</b><br>XIAO M. WU        | <b>Art Unit</b><br>2674          |  |

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

### Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

### Status

- 1) ☒ Responsive to communication(s) filed on 20 December 2005.
- 2a) ☒ This action is **FINAL**.                      2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

### Disposition of Claims

- 4) ☒ Claim(s) 1-3 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1-3 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

### Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on \_\_\_\_\_ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

### Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All    b) ☐ Some \*    c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

### Attachment(s)

- |                                                                                         |                                                                             |
|-----------------------------------------------------------------------------------------|-----------------------------------------------------------------------------|
| 1) <input type="checkbox"/> Notice of References Cited (PTO-892)                        | 4) <input type="checkbox"/> Interview Summary (PTO-413)                     |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)    | Paper No(s)/Mail Date. _____                                                |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| Paper No(s)/Mail Date _____                                                             | 6) <input type="checkbox"/> Other: _____                                    |

## DETAILED ACTION

### *Claim Rejections - 35 USC § 102*

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

2. Claims 1-3 are rejected under 35 U.S.C. 102(b) as being anticipated by Kihara et al. (US Patent No. 5,889,504).

As to claim 1, Kihara discloses a display driving circuit (fig. 4), comprising: a plurality of driving stages, wherein the driving stages are electrically coupled in serial, (see Fig. 7) and each of the driving stages comprises a conducting path so as to transmit an electric signal from a previous driving stage to a next driving stage (e.g. right normal shift register); and a plurality of driving lines (the column line 1 to each pixel cell), wherein each of the driving lines corresponds to one of the driving stages respectively (see Fig. 7), and the driving line is electrically coupled to an output terminal of a corresponding driving stage (Fig. 7); wherein it is characterized in that a redundant device (e.g. right redundant shift register) is installed in each part of the driving stages, respectively, and the redundant device is capable of supplying an extra conducting path to transmit an electric signal from the previous driving stage to the next driving stage via the current driving stage while the original conducting path in the corresponding driving stage is broken (e.g. when the normal shift register is broken).

As to claim 2, Kihara discloses the redundant device (e.g. right redundant circuit as shown in Fig. 4) is added into a driving stage subsequent to a plurality of preceding driving

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stages that are installed separately departing from a predetermined number of the driving stages with each other (see Fig. 4).

As to claim 3, Kihara discloses the redundant device (e.g. right redundant circuit as shown in Fig. 4) is added to a plurality of contiguous driving stages subsequent to a plurality of preceding driving stages that are installed separately, departing from a predetermined number of the driving stages with each other (see Fig. 4).

### *Response to Arguments*

3. Applicant's arguments filed 12/20/2006 have been fully considered but they are not persuasive. Applicant argues that the element "redundant device" in claims 1-3 in the present invention is referred to a plurality of TFTs or transistors located within a single shift register as illustrated in Fig. 4 of the present invention and in Paragraph [0021] of the present invention, and on the hand, the "redundant device" in Kihara is defined as a shift register that is complete, which is namely the right redundant shift register SR4 as found in Fig. 7 in Kihara. Then, applicant concludes that the "redundant device" is referred to a shift register, whereas, the "redundant device" in the present invention is referred to a transistor instead, therefore, it is clearly that the "redundant device" is NOT anticipated by Kihara. These arguments are not persuasive because claims do not require that the "redundant device" includes a plurality of TFTs or transistors located within a single shift register. Claims 1-3 only require that the redundant device is capable of supplying an extra conducting path to transmit an electric signal from the previous driving stage to the next driving stage via the current driving stage while the original conducting path in the corresponding driving stage is broken. Kihara clearly teaches this claimed limitation because the redundant device of Kihara (e.g. right redundant shift register is

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capable of supplying an extra conducting path to transmit an electric signal from the previous driving stage to the next driving stage via the current driving stage while the original conduct path (e.g. right normal shift register) in the corresponding driving stage is broken.

### ***Conclusion***

4. **THIS ACTION IS MADE FINAL.** Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to XIAO M. WU whose telephone number is 571-272-7761. The examiner can normally be reached on 6:30 am to 4:00 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, PATRICK EDOUARD, can be reached on 571-272-7603. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

X.W.

February 27, 2006



**XIAO M. WU**  
**Primary Examiner**  
**Art Unit 2674**